

DUAL EMITTER TRANSISTOR WITH ESD PROTECTION

Abstract of the Disclosure

The invention provides a method and circuit design for providing ESD protection. Specifically, a dual emitter transistor is used to provide bipolar operation and ESD protection. The first emitter provides bipolar operation under normal operating conditions. The second emitter provides an alternative current path for ESD protection.

Figures

Figure 1: A vertical list of text elements, possibly a table of contents or a list of items, arranged in a single column. The text is small and difficult to read, but appears to be organized in a structured manner.